

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Stephan Voges et al.

Application No.: 09/521,280

Filed: 7 March 2000

For : METHODS AND APPARATUS FOR HARDWARE

SIMULATION AND DESIGN VERIFICATION

Group Art Unit : 2122

Examiner : ERIC B. KISS

Mail Stop AMENDMENT Commissioner for Patents P.O. BOX 1450 Alexandria, VA 22313-1450

## **REPLY TO OFFICE ACTION**

Sir:

Applicants reply to the Office Action dated 10 August 2005. Please amend the application as follows: